

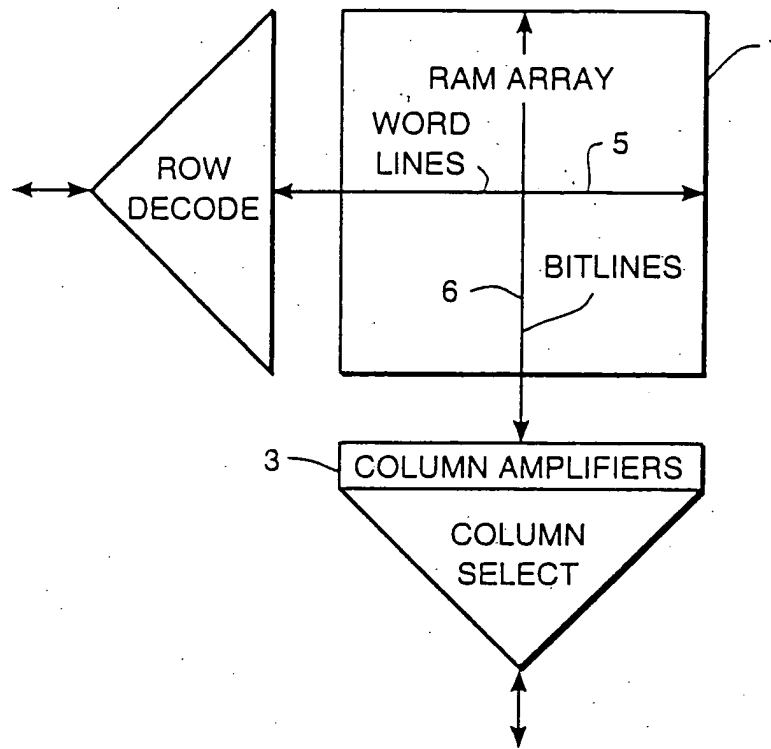
FIG 1

FIG. 2

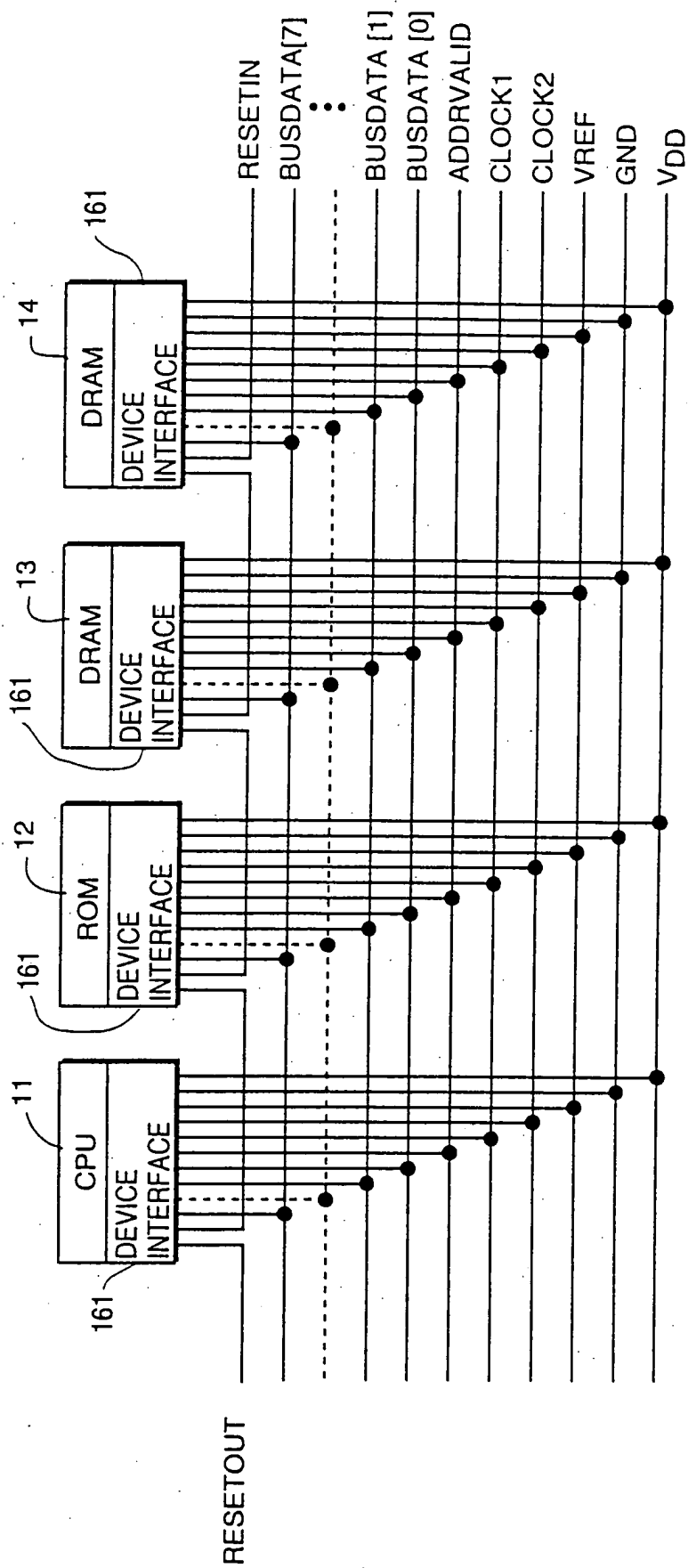
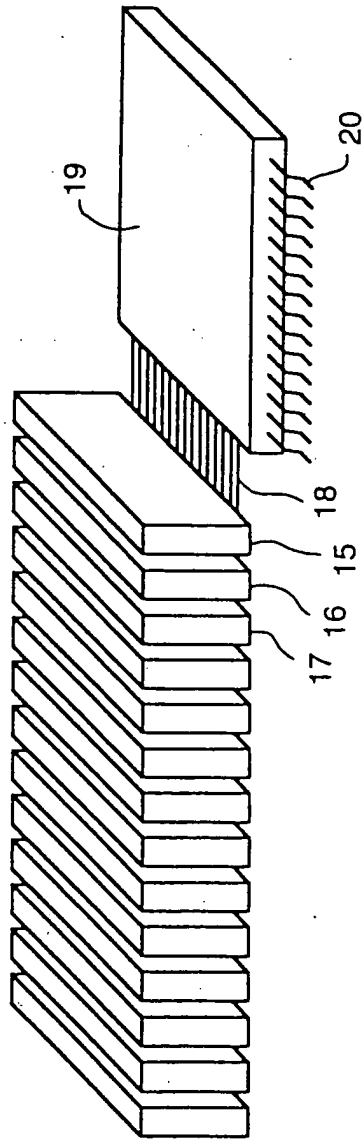


FIG 3



REGULAR ACCESS

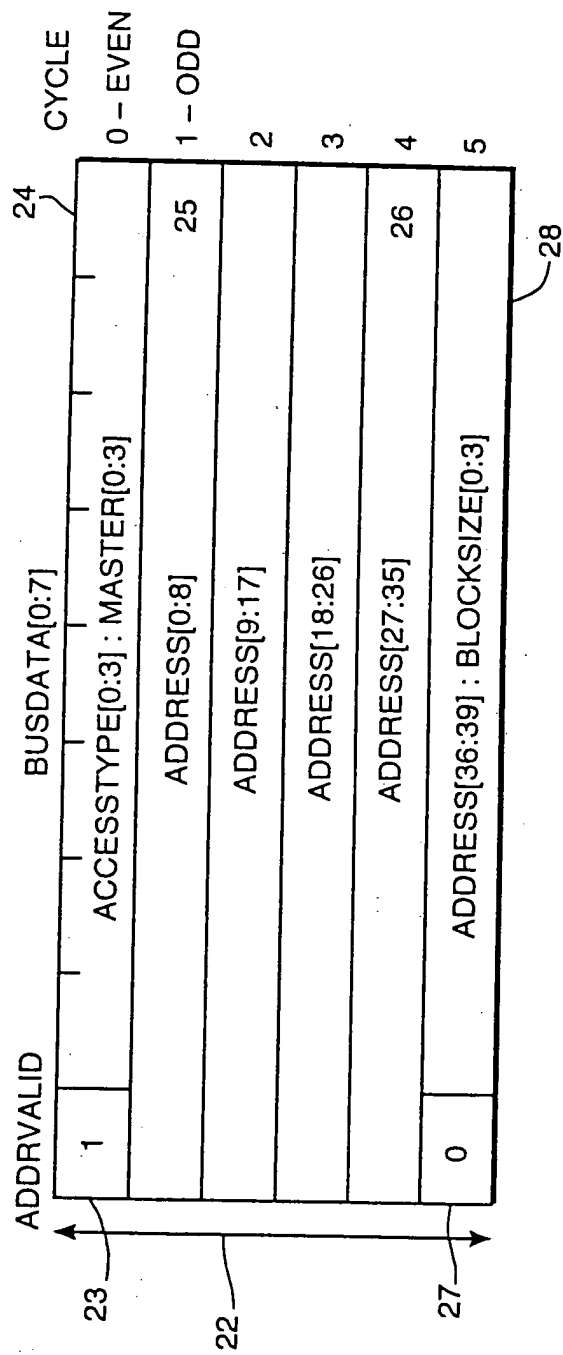


FIG 4

REJECT (NACK) CONTROL PACKET

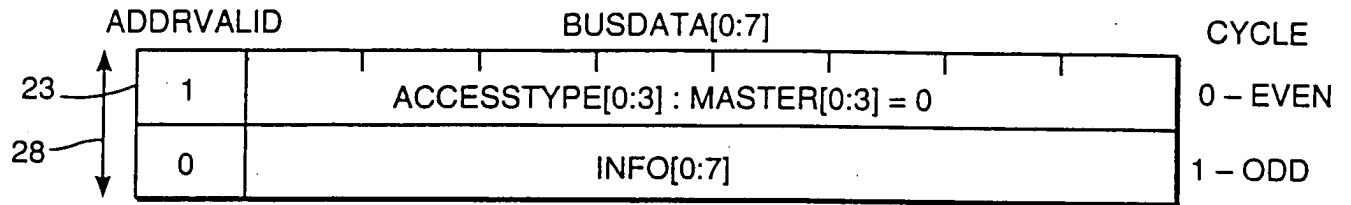


FIG 5

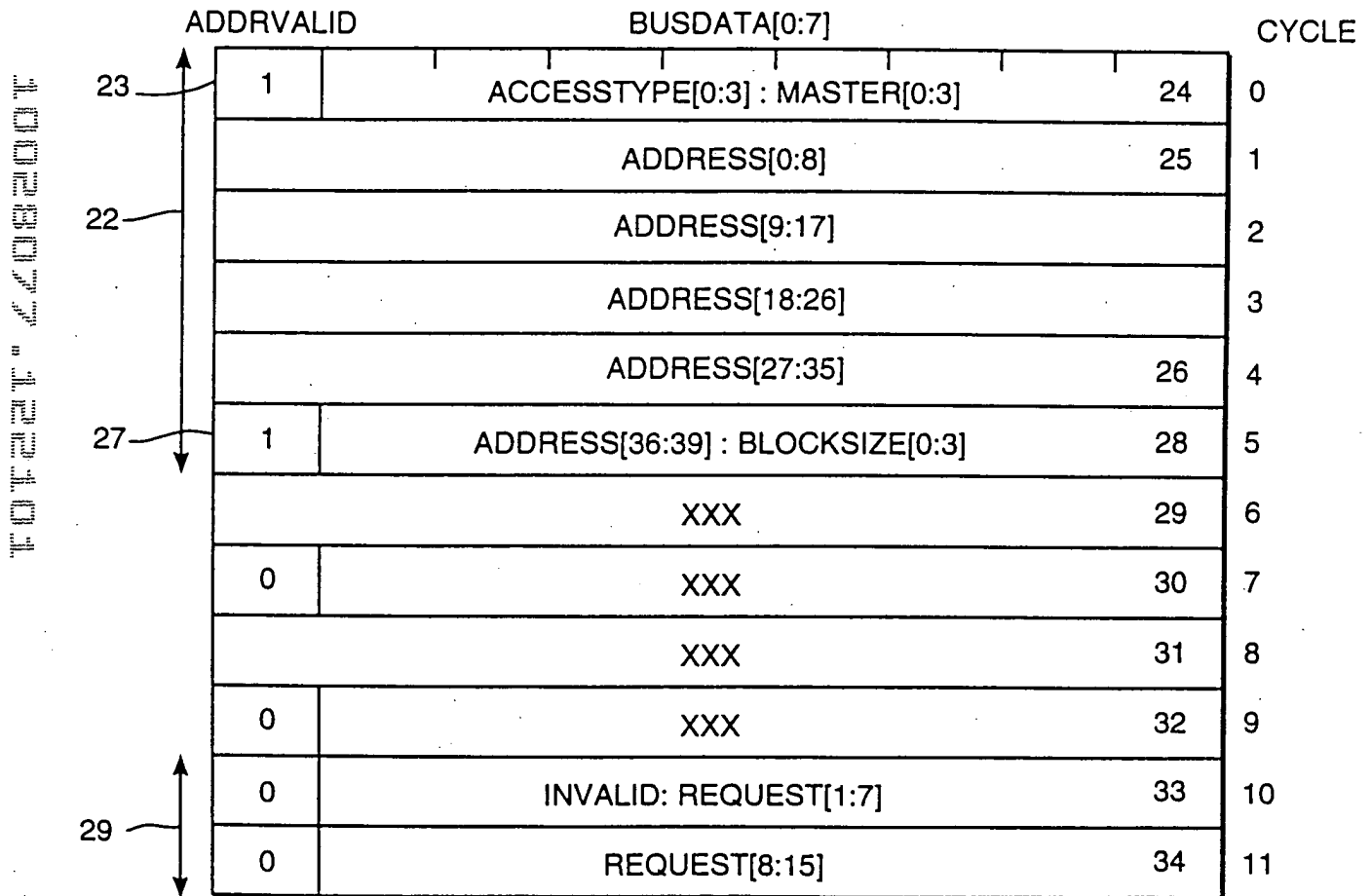


FIG 6

10028077.123101

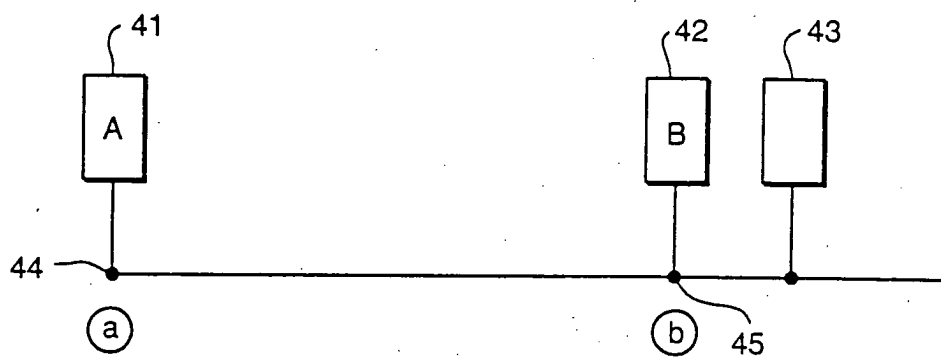
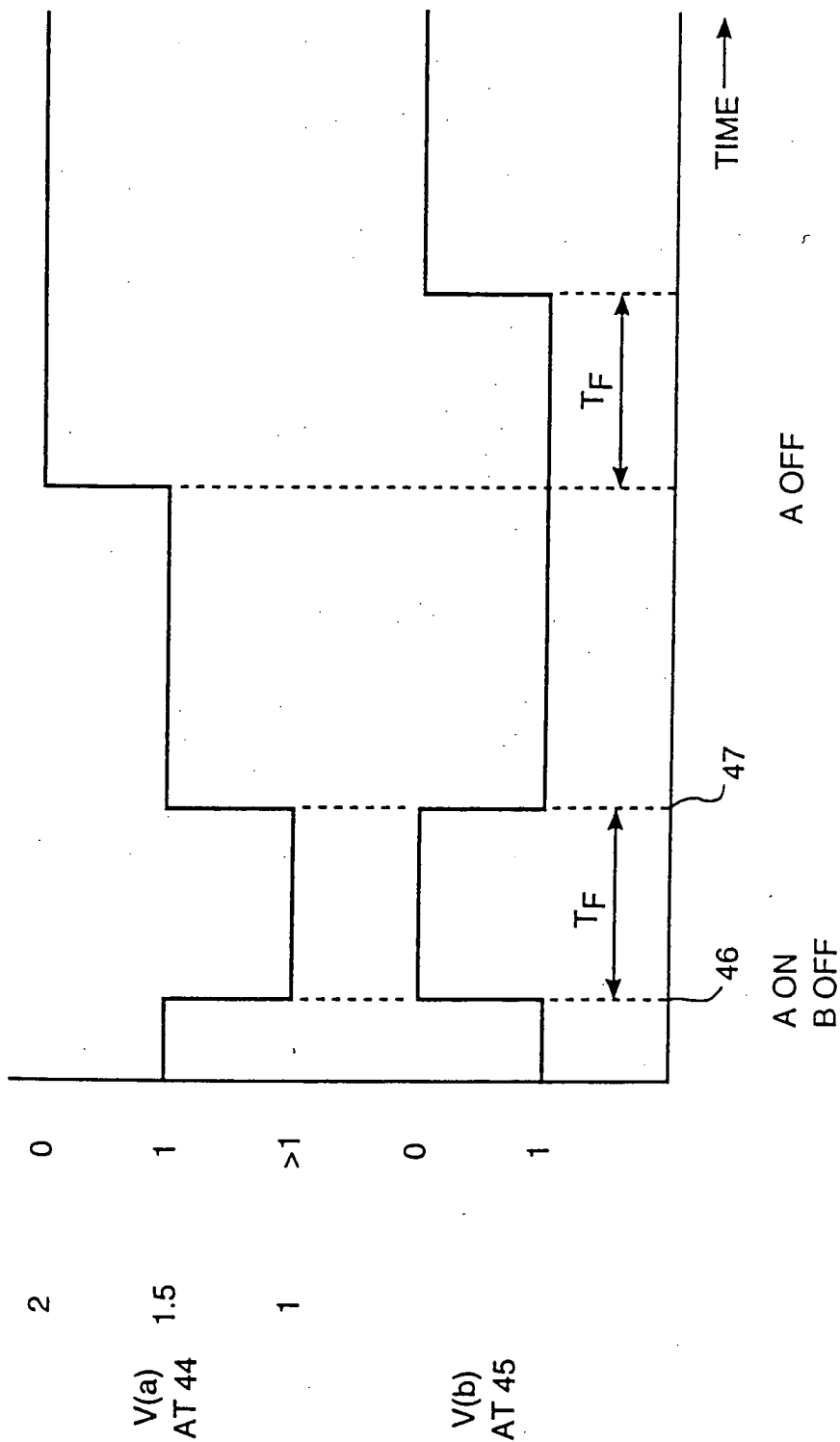


FIG 7A

VOLTAGE LOGICAL
VALUE



FILE 7B

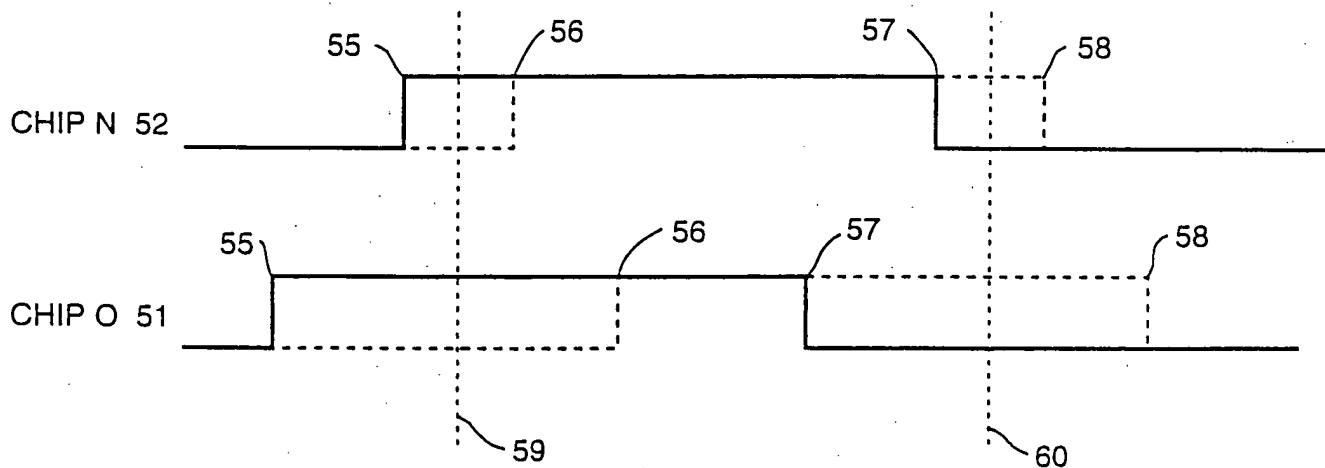
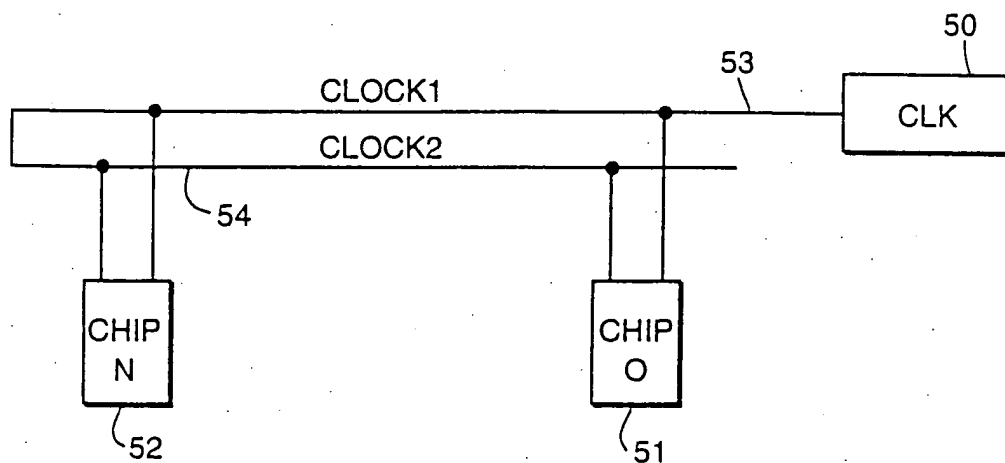
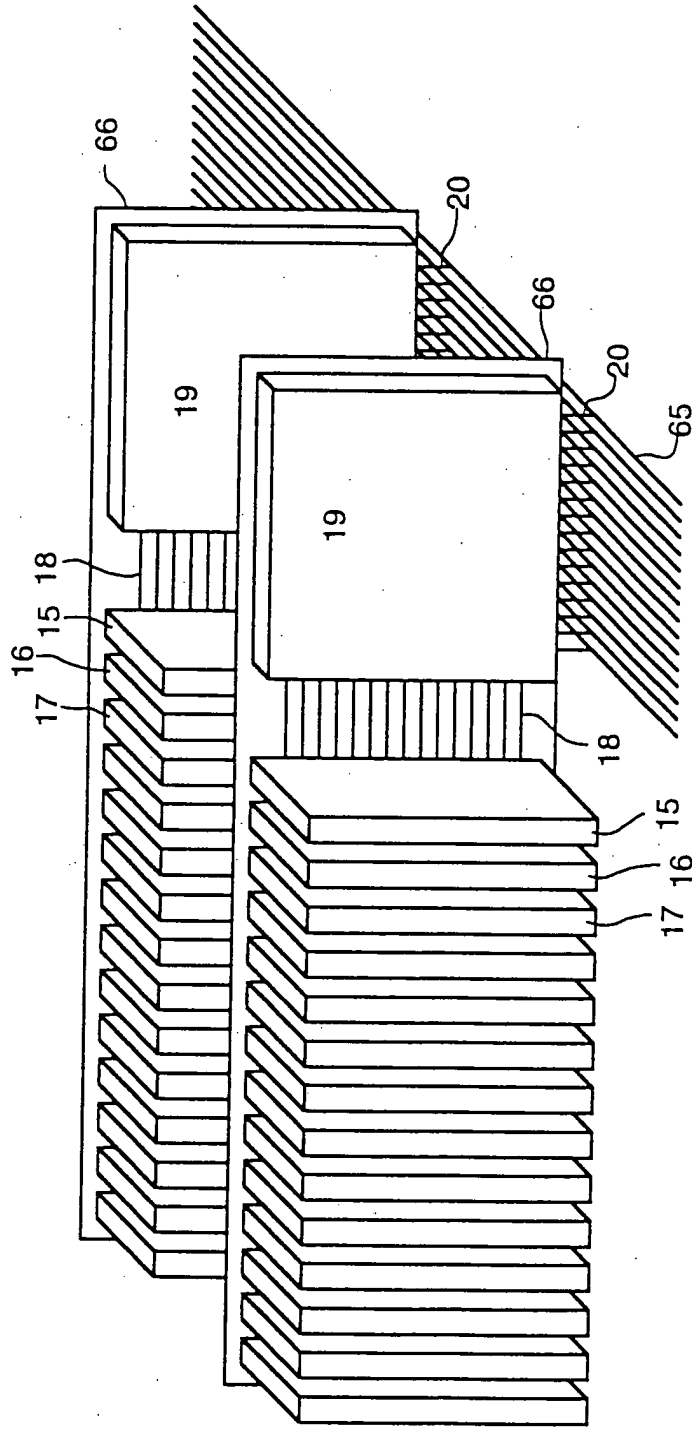


FIG. 2



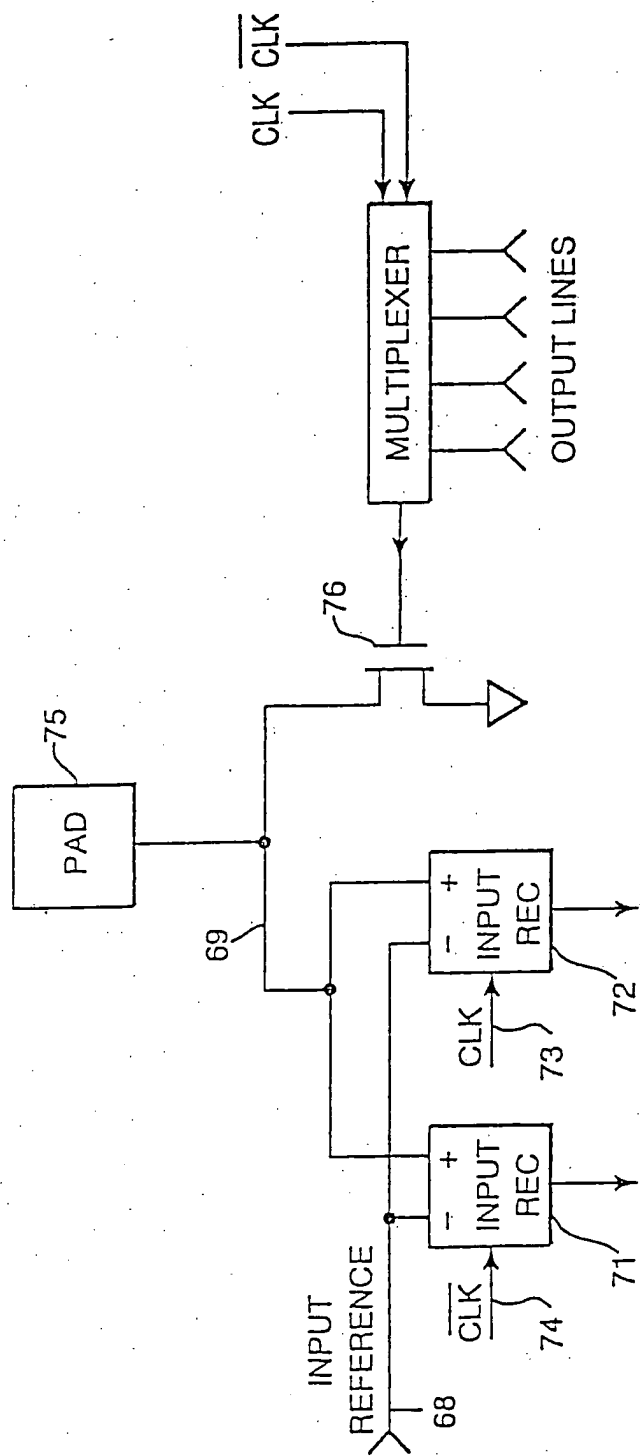
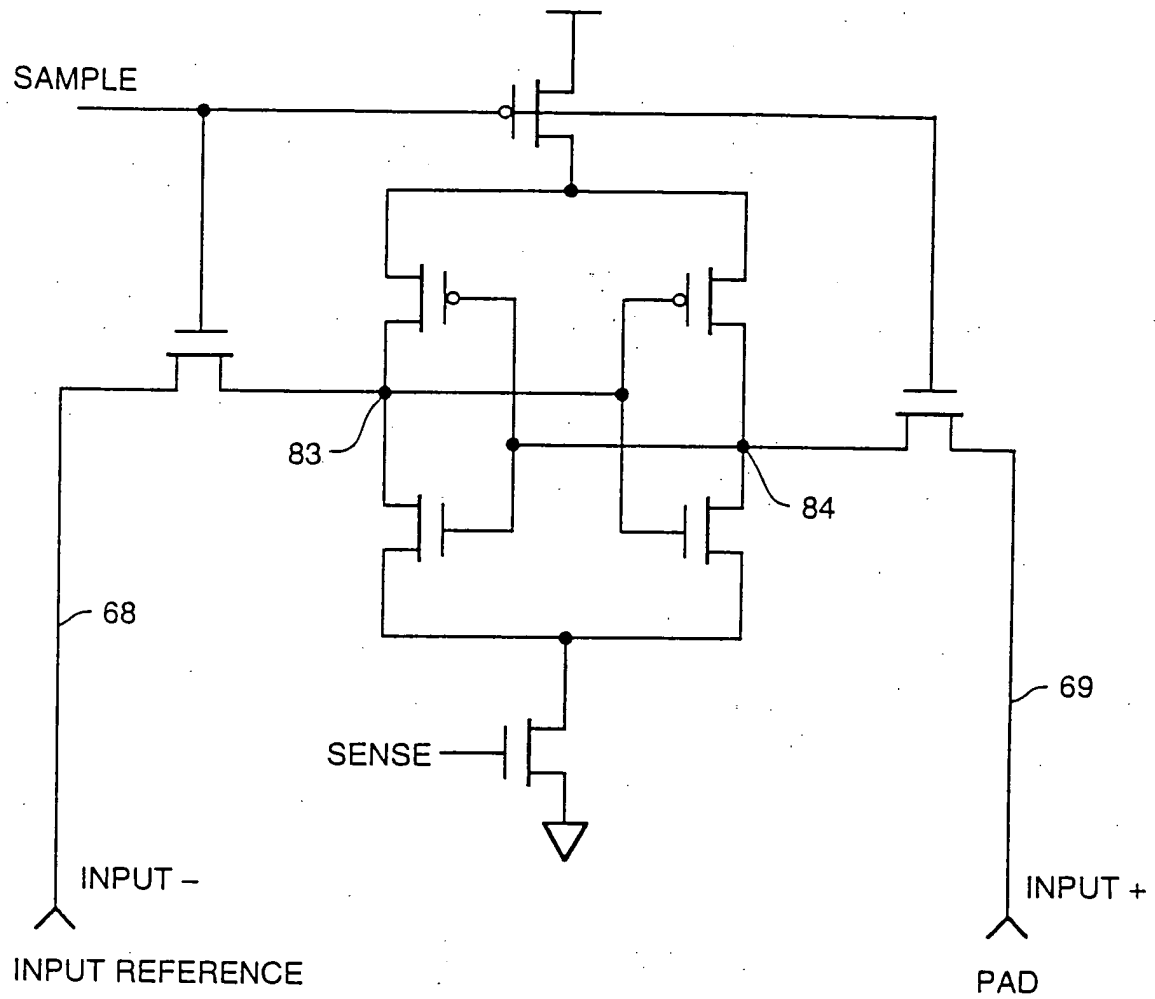


FIG 10

FIG 11



1002807-122101

FIG 12

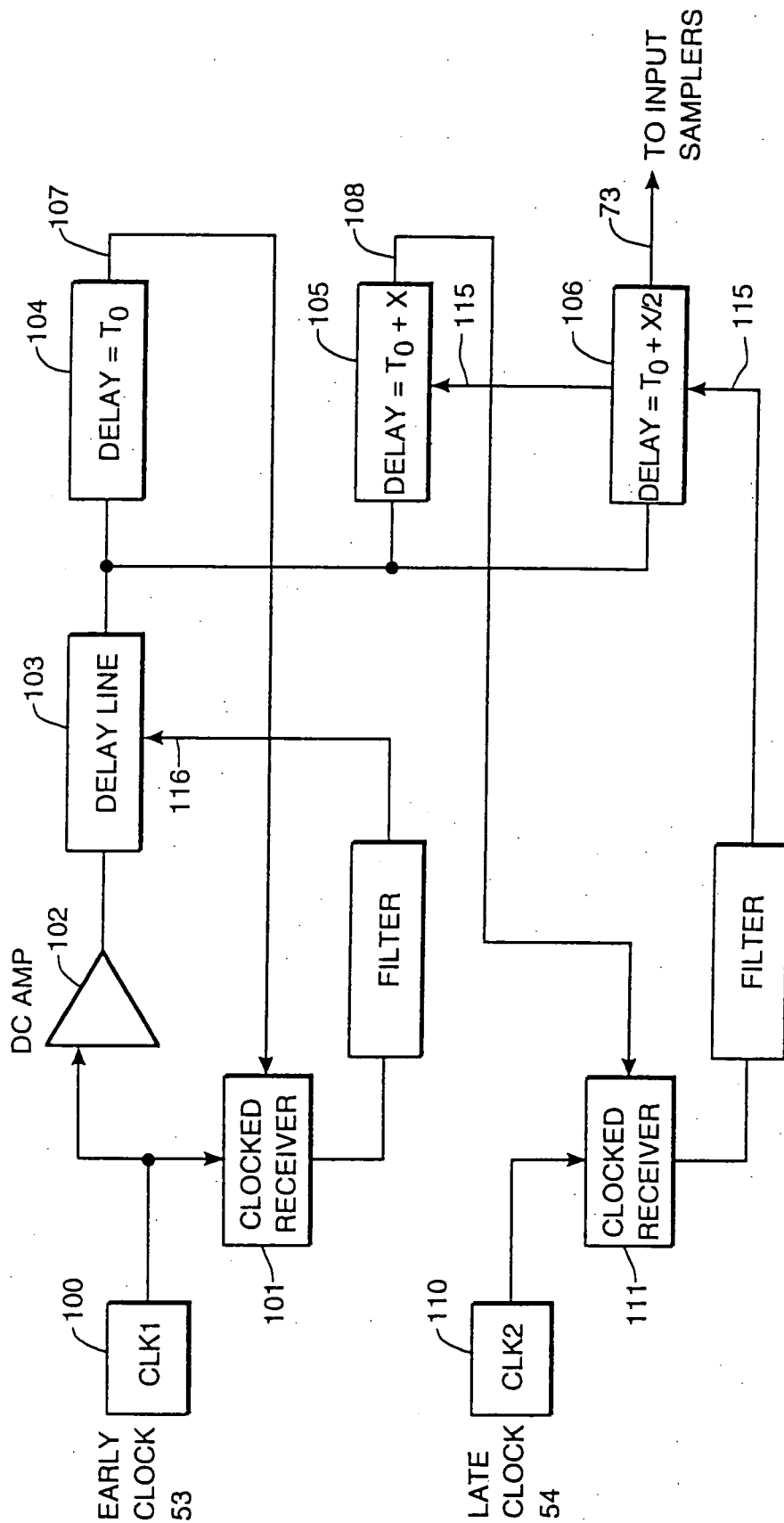
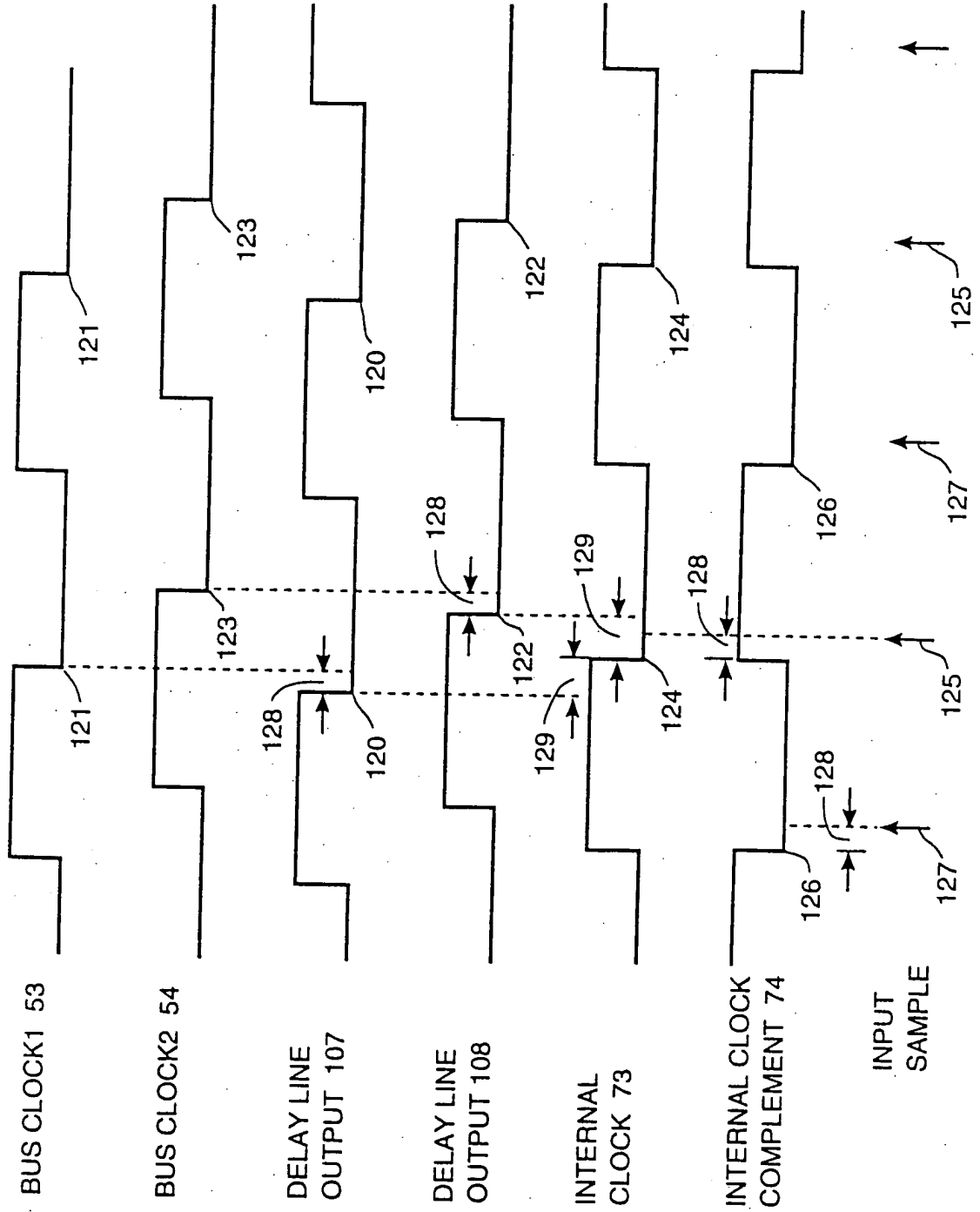


FIG 13



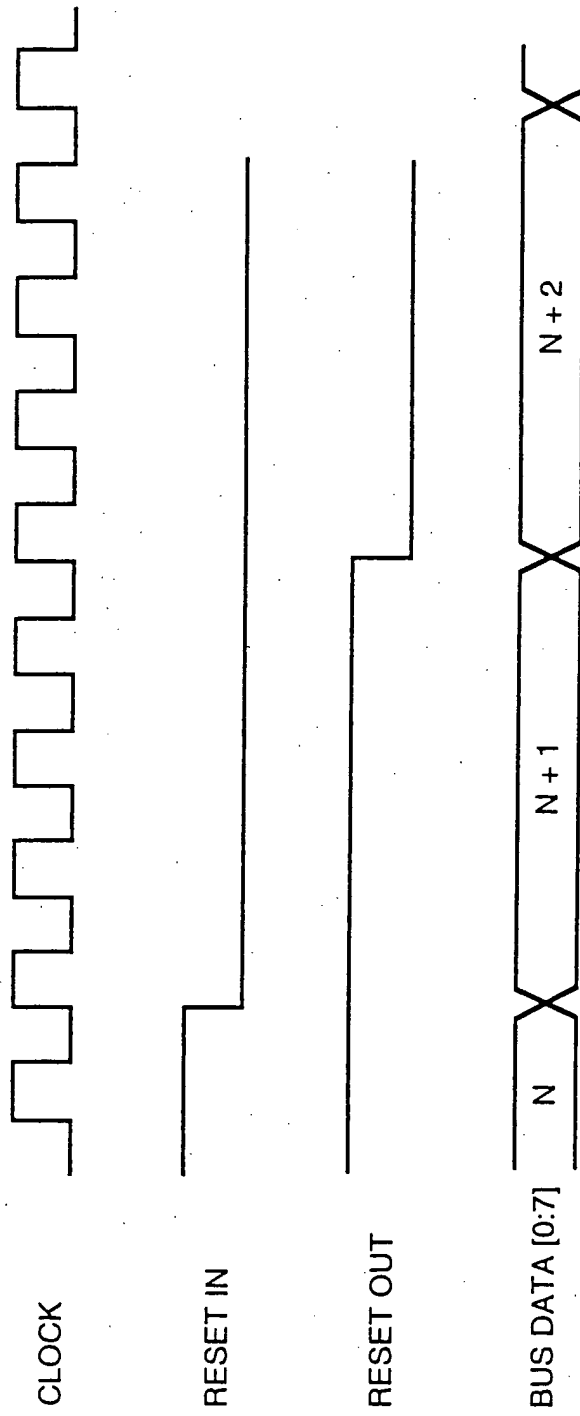
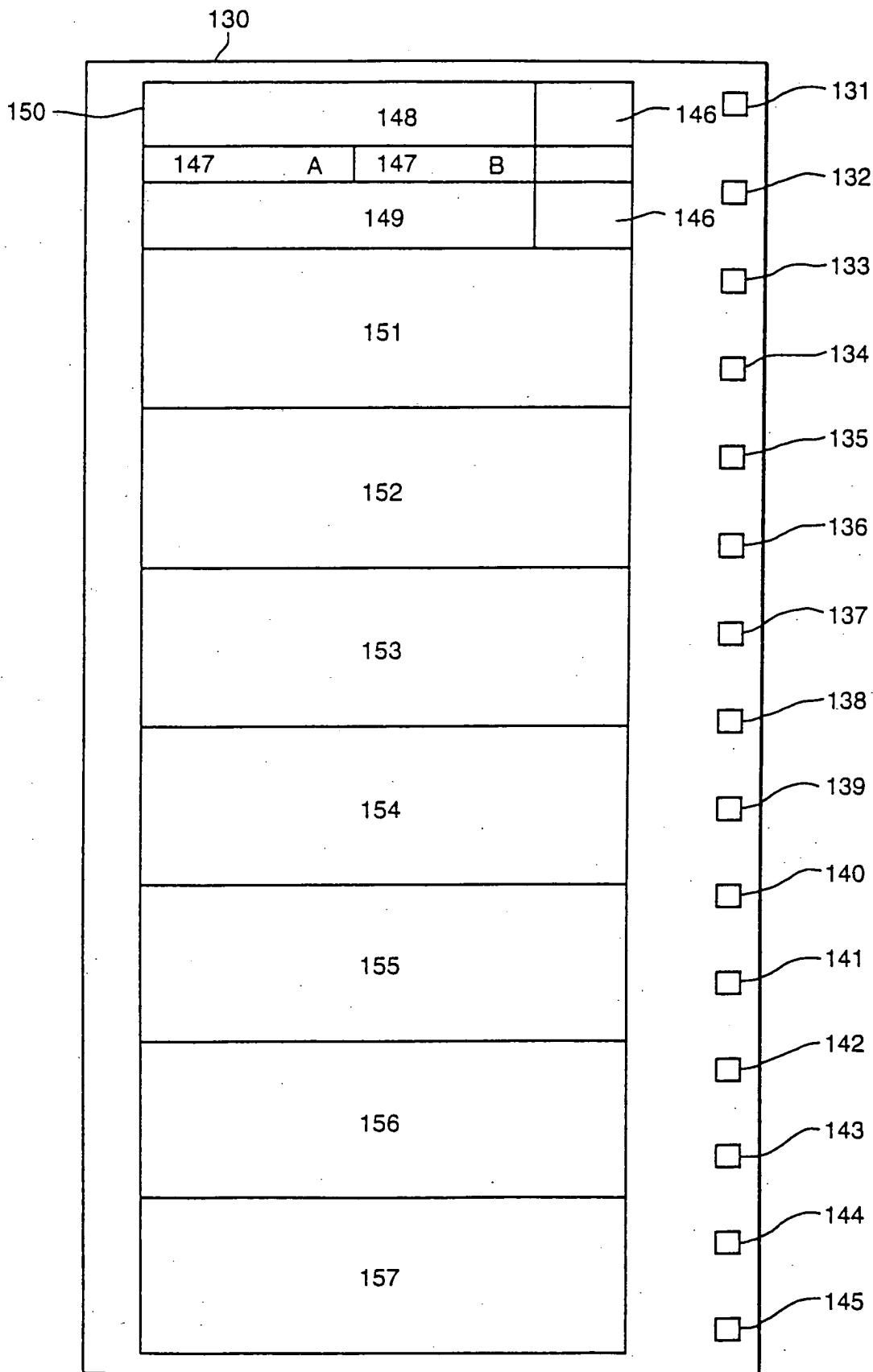


FIG 15



1002807-123101

FIG. 10

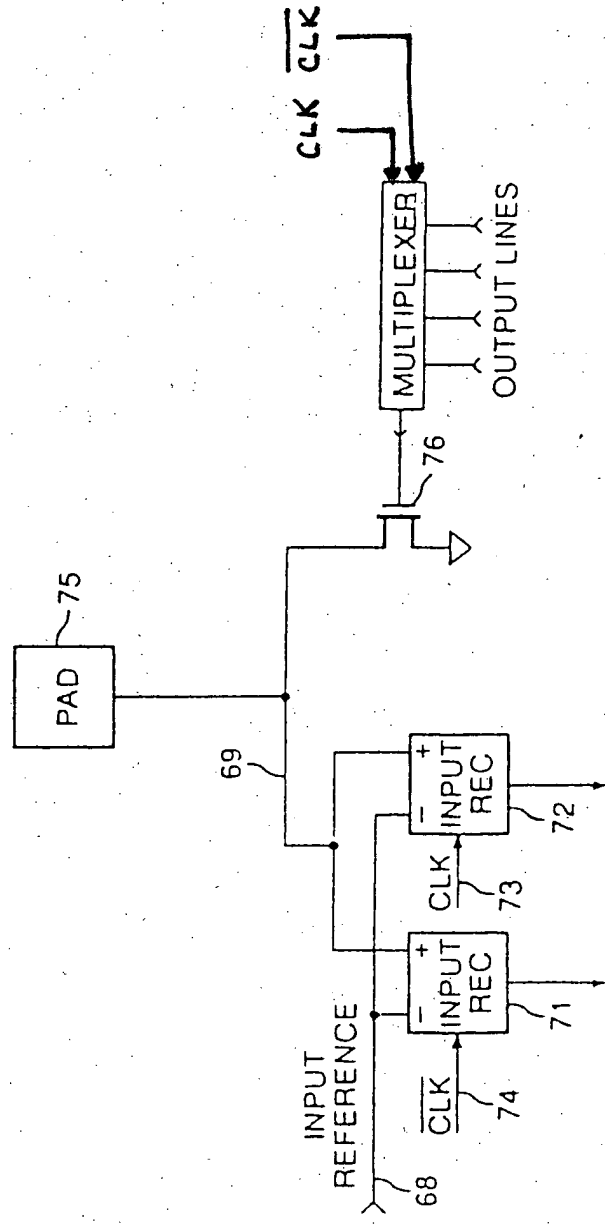


FIG. 10